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P.O. B x 2938

Minneapolis, MN 55402

Telephone (612) 373-6900

Facsimile (612) 339-3061

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Time: 2:05pm
(Minneapolis, Minn.)TO: Commissioner for Patents
Attn: Wai-Sing Louie
Patent Examining Corps
Facsimile Center
Washington, D.C. 20231FROM: Edward J. Brooks, IIIOUR REF: 303.382US2TELEPHONE: (612) 373-6913FAX NUMBER (703) 308-7722

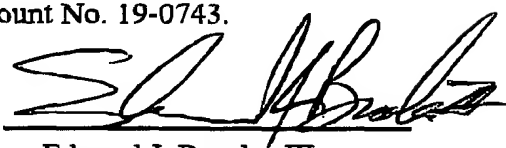
* Please deliver to Examiner Wai-Sing Louie in Art Unit 2814. *

Document(s) Transmitted: A Terminal Disclaimer (3 pgs.)Total pages of this transmission, including cover letter: 4 pgs

If you do NOT receive all of the pages described above, please telephone us at 612-373-6900, or fax us at 612-339-3061.

In re. Patent Application of: Joseph E. Geusic et al.Examiner: Wai-Sing LouieSerial No.: 09/618,648Group Art Unit: 2814Filed: July 18, 2000Docket No.: 303.382US2Title: INTEGRATED CIRCUITS USING OPTICAL WAVEGUIDE INTERCONNECTS
FORMED THROUGH A SEMICONDUCTOR WAFER AND METHODS FOR
FORMING SAME

Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

By: 
Name: Edward J. Brooks, III
Reg. No.: Reg. No. 40,925

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.

Tina M. Kohout
Tina M. Kohout8/12/02
Date of Transmission

S/N 09/618,648IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Joseph E. Geusic et al.

Serial No.: 09/618,648

Filed: July 18, 2000

Title: INTEGRATED CIRCUITS USING OPTICAL WAVEGUIDE
INTERCONNECTS FORMED THROUGH A SEMICONDUCTOR WAFER
AND METHODS FOR FORMING SAME

Examiner: Wai-Sing Louie

Group Art Unit: 2814

Docket: 303.382U

PATENT**TERMINAL DISCLAIMER
APPROVED****AUG 19 2002****TECHNOLOGY CENTER 2800
SPECIAL PROGRAM CENTER**TERMINAL DISCLAIMERCommissioner for Patents
Washington, D.C. 20231

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✓ I, Edward J. Brooks, III, am the attorney of record for the above identified patent application as evidenced by the Power of Attorney filed in the present application on July 18, 2000. I am making this petition on behalf of Micron Technology, Inc., the assignee of the present invention. As the attorney of record, I am empowered to act on behalf of the assignee and, in accordance with 37 C.F.R. § 1.321(b)(iv), to sign this terminal disclaimer.

Certificate Under 37 C.F.R. §3.73(b)

Your petitioner, Micron Technology, Inc., certifies that they are the owner of the entire right, title and interest in and to the above-identified patent application (Serial No. 09/618,648) and to U.S. Patent No. 6,198,168. Your petitioner owns the entire right, title, and interest of these applications by nature of the assignments executed and filed for both of these applications. ✓ The assignment for U.S. Patent No. 6,198,168 was recorded on January 20, 1998 on Reel 9037, Frames 0757 - 0766, with the United States Patent and Trademark Office and the assignment for Serial No. 09/618,648 was recorded on August 13, 1998 on Reel 9391, Frames 0366 - 0375, with the United States Patent and Trademark Office.

The undersigned representative of the assignee has reviewed the evidentiary documents of title and certifies that to the best of assignee's knowledge and belief, title is in the assignee, Micron Technology, Inc., seeking to take the action set forth in this disclaimer.

TERMINAL DISCLAIMER

Serial Number: 09/618,648

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WAFER AND METHODS FOR FORMING SAME

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Dkt: 303.382US2

Terminal Disclaimer

Your petitioner hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the above-identified patent application, which would extend beyond the expiration date of the full statutory term, as presently shortened by any terminal disclaimers, of U.S. Patent No. 6,198,168. Petitioner hereby agrees that any patent so granted on the above-identified application shall be enforceable only for and during such period that the legal title to U.S. Patent No. 6,198,168 and the legal title of the above-identified application and any patent granted thereon remain common, this agreement to run with any patent granted on the above-identified application and to be binding upon the grantee, its successors and assigns.

Limitations on the Disclaimer

Your petitioner does not disclaim any terminal part of any patent granted on the above-identified application prior to the expiration date of the full statutory term as presently shortened by any terminal disclaimer of U.S. Patent No. 6,198,168 in the event that it later expires for failure to pay a maintenance fee, is held unenforceable, is found invalid, is statutorily disclaimed in whole or terminally disclaimed under 37 C.F.R. § 1.321(a), has all claims canceled by a reexamination certificate, or is otherwise terminated prior to the expiration date of its statutory term as presently shortened by any terminal disclaimer, except for the separation of legal title as stated hereinabove.

TERMINAL DISCLAIMER

Serial Number: 09/618,648

Filing Date: July 18, 2000

Title: INTEGRATED CIRCUITS USING OPTICAL WAVEGUIDE INTERCONNECTS FORMED THROUGH A SEMICONDUCTOR
WAFER AND METHODS FOR FORMING SAME

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Fee Status

Please charge Deposit Account No. 19-0743 in the amount of \$110.00, which is required under 37 C.F.R. §1.20(d) to file a statutory disclaimer.

Respectfully submitted,

JOSEPH E. GEUSIC ET AL.

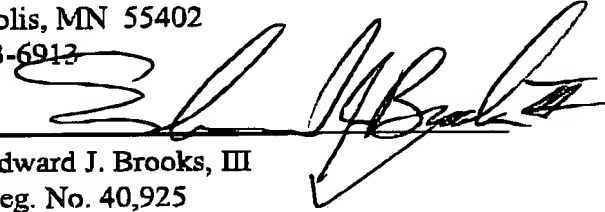
By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6913

Date

8/12/2002

By


Edward J. Brooks, III
Reg. No. 40,925

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Daniel P. Vega